



U1			
P0[7]	1	P0[7]	VCC 20 psVCC
P0[5]	2	P0[5]	P0(6)
P0[3]	3	P0[3]	18 P0(4)
P0[1]	4	P0[1]	17 P0(2)
GND	5	GND/SMP	16 P0(0)
P1[7]	6	P1[7]	15 Xres
P1[5]	7	P1[5]	14 P1(6)
P1[3]	8	P1[3]	13 P1(4)
PGC	9	P1[3]	12 P1(2)
GND	10	GND	11 PGD

CY8C22213/24223

U2			
P0[7]	1	P0[7]	VCC 28 psVCC
P0[5]	2	P0[5]	27 P0(6)
P0[3]	3	P0[3]	26 P0(4)
P0[1]	4	P0[3]	25 P0(2)
P2[7]	5	P2[7]	24 P0(0)
P2[5]	6	P2[5]	23 P2(6)
P2[3]	7	P2[5]	22 P2(4)
P2[1]	8	P2[3]	21 P2(2)
GND	9	P2[1]	20 P2(0)
P1[7]	10	GND/SMP	19 Xres
P1[5]	11	P1[7]	18 P1(6)
P1[3]	12	P1[5]	17 P1(4)
PGC	13	P1[3]	16 P1(2)
GND	14	GND	15 PGD

CY8C1534/24423

U3			
P0[7]	sVCC	P0[7]/AI	VCC 48 p
P0[5]	2	P0[5]/AIO	AU/P0(6)
P0[3]	3	P0[3]/AIO	AIO/P0(4)
P0[1]	4	P0[1]/AI	AIO/P0(2)
P2[7]	5	P2[7]/AI	AU/P0(0)
P2[5]	6	P2[5]	43 P2(6)
P2[3]	7	P2[5]	42 P2(4)
P2[1]	8	P2[3]/AI	AU/P2(2)
P4[7]	9	P2[1]/AI	AU/P2(0)
P4[5]	10	P4[7]	39 P4(6)
P4[3]	11	P4[5]	38 P4(4)
P4[1]	12	P4[3]	37 P4(2)
13		P4[1]	36 P4(0)
P3[7]	14	SMP	35 Xres
P3[5]	15	P3[7]	34 P3(6)
P3[3]	16	P3[5]	33 P3(4)
P3[1]	17	P3[3]	32 P3(2)
P5[3]	18	P3[1]	31 P3(0)
P5[1]	19	P5[3]	30 P5(2)
P1[7]	20	P5[1]	29 P5(0)
P1[5]	21	P1[7]/SCL	P5(0)
P1[3]	22	P1[5]/SDA	P1(6)
PGC	23	P1[3]	EXTCLK/P1(4)
GND	24	GND	OSC2/P1(2)
		GND	OSC1/P1(0)

CY8C27643

